Who can Attend it

Faculty Member, UG and PG students of Electronics & Communication Engineering

Registration

No registration fee for participants.

Mode of Selection

Participants are requested to register for the workshop by filling up on-spot registration form on 18th July 2016

Important Dates

Last date for registration: 18th July, 2016

Contact Person

Mr. Sohan Ghorai Ph No: 9038505140

Mr. Abhijit Ghosh Ph No: 9748179097

Mr. Surajit Bari Ph. No: 9432128450

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Voltage Digital Samples Analog Signal Time

Chief Patron:

• Sardar Jodh Singh , Chairman, JIS Group

Patrons

- Mr. Taranjit Singh, MD, JIS Group
- Prof. (Dr.) S.M. Chatterjee, Chairman, BOG, NIT
- Mr. S. S. Dutta Gupta, Director, JIS Group
- Prof. (Dr.) Asit Guha, Advisor to MD, JIS Group
- Mr. U. S. Mukherjee, Dy. Director, JIS Group

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- Prof (Dr) P. K. Banerjee, Advisor, JIS Group
- Prof (Dr) A. K. Mallick, Professor, ECE Dept.
- Prof (Dr) J. K. Das, Professor, ECE Dept., NIT

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Dr. Saradindu Panda, Head ECE Dept. , NIT
Coordinator:

- Mr. Sohan Ghorai, Asst. Prof., ECE Dept., NIT
- Mr. Abhijit Ghosh, Asst. Prof., ECE Dept., NIT

Technical Committee:

Mr. Abhijit Dey, Technical Asst. ECE Dept., NIT

Finance Committee:

Finance Committee:

- Prof. (Dr.) Sumit Nandi, TEQIP Coordinator, BSHU, NIT
- Mr. Surajit Bari, Asst. Prof., ECE Dept., NIT

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- Mr. Pranab Hazra, Asst. Prof., ECE Dept., NIT
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- Mrs. Swati Barui, Asst. Prof., ECE Dept., NIT
- Mrs. Payel Biswas, Asst. Prof., ECE Dept., NIT
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- Mr. Puspak Pain , Asst. Prof. , ECE Dept. ,NIT
- Ms. Rimpi Datta, Asst. Prof., ECE Dept., NIT
- Mrs. Moupali Roy , Asst. Prof. , ECE Dept. ,NIT









Workshop

On

Fundamentals of Electronics & Communication Systems

18th July - 22nd July 2016

Organized by

ECE Department

Narula Institute of Technology

Agarpara, Kolkata-700109



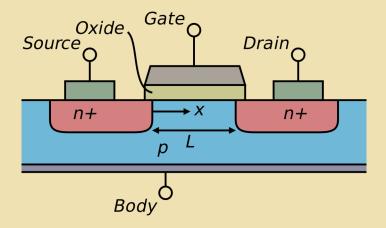
About the Department

The department of ECE offers premier professional technical training that keeps in pace with the latest developments in the field of Electronics and Communication Engineering. We have excellent infrastructure, highly experienced faculties and professionals, associated with research work which collectively ensure dynamic and vibrant environment in the campus. Some of the Faculty members of this department went to attend International Conferences in abroad. The department organized so many International & National Educational Programs (i.e. IEEE sponsored Seminars, Workshops, Faculty Development Programs and one day educational Industrial tours). There are so many events like Paper presentation, Poster presentation, Technical Quiz contest, Technical Report writing Contest & Technical Workshop cum contest associated with IIT, Bombay are organized for the students by this department. Department has established a student chapter of IETE and it is running successfully. There are more than 150 publications are done by the faculty & students in National & International Journals & Conferences. Furthermore three MODROB Projects funded by AICTE are going on in this department. Many of our Alumni are settled in abroad and also many of the students are GATE qualified and placed in so many reputed Govt. sectors and Private Sectors

Company and Research Organizations. There is also separate Departmental Library apart from the Central Library in the ECE department. There are so many high valued simulation software are present in our department like Tanner SPICE, CADENCE, Matlab, Visual TCAD, HFSS, Qualnet, Active HDL, MPLS, Altera & Xilinx ISE, TMS Kit with Code Composer Studio, ARM kit and Advanced FPGA kit etc. We have very prestigious PCB design Laboratory with full set up.

Course Objective

The workshop intends to provide opportunities to students to enrich their intellectual validity and further their professional growth. The major focus of the workshop is for the upgradation of outcome based teaching & learning skills in the field of electronics & Communication Systems. Moreover this workshop aims to inculcate learning values in students and monitoring their progress in career.



Workshop Coverage

- ♦ Digital Filter Design
- ♦ MOSFET
- ♦ Transmission Line
- ♦ Analog Communication
- ♦ Microcontrollers & Applications
- Op-Amp fundamentals and its applications
- ♦ Digital Electronics
- ♦ DSP

Resource Person

- Prof.(Dr.) C.K.Sarkar, JU
- ♦ Prof.(Dr.) S. Sanyal, JU
- ♦ Prof. (Dr.) Monojit Mitra, IIEST Shibpur
- ♦ Prof.(Dr.) Angsuman Sarkar, KGEC.
- ♦ Prof. (Dr.) P. K. Banerjee, Ex. JU
- Prof. (Dr.) Sheli Sinha Chaudhury, JU
- ♦ Prof. (Dr.) S. Bhaumick, IIEST Shibpur
- ♦ Prof. (Dr.) Asik Pal, CU